

Fig. 1

સ્મિટ કોર્પુસ "અન્ડ કેવી રીતે

SMT CPU 100

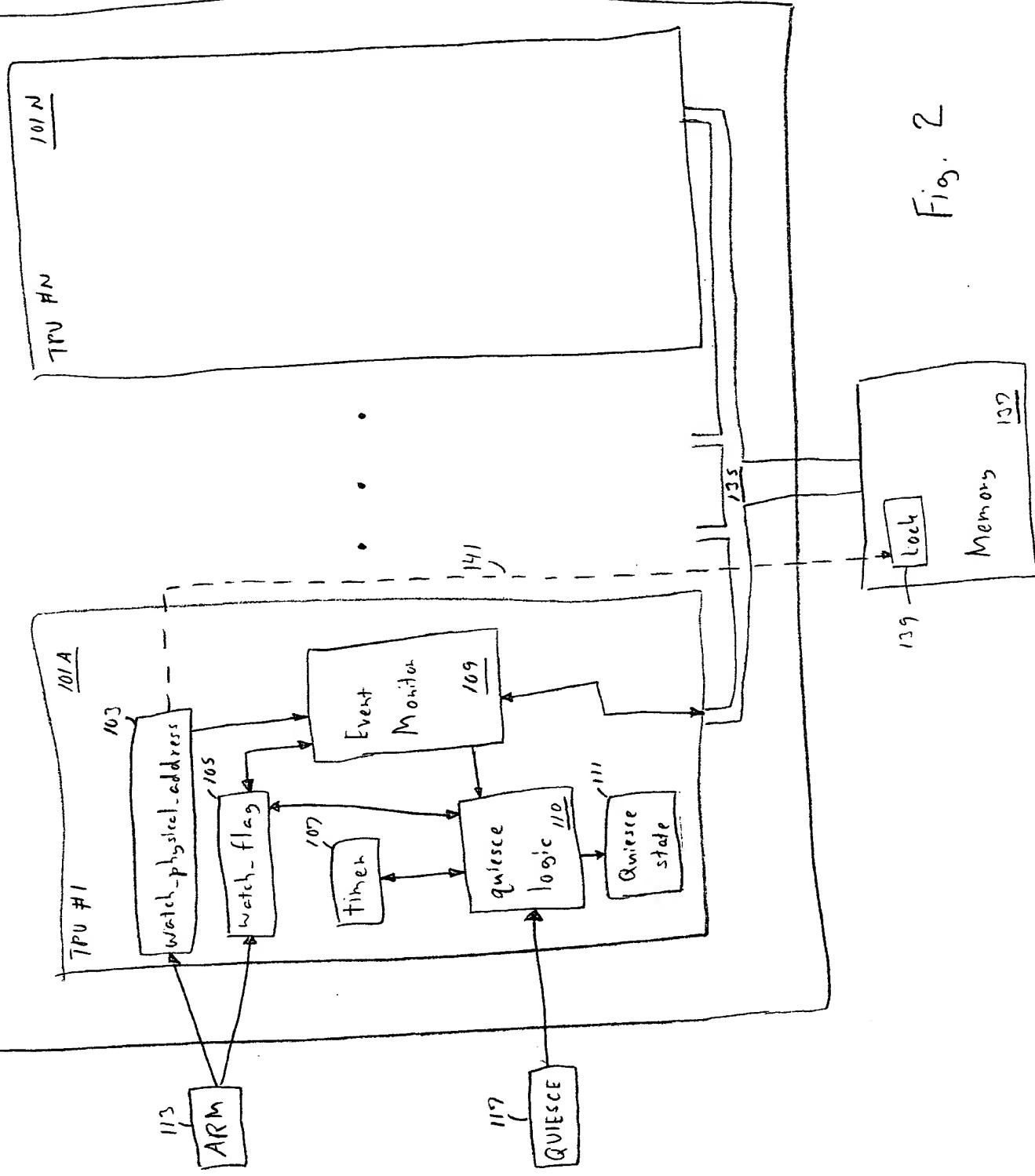


Fig. 2

Processor Control Unit

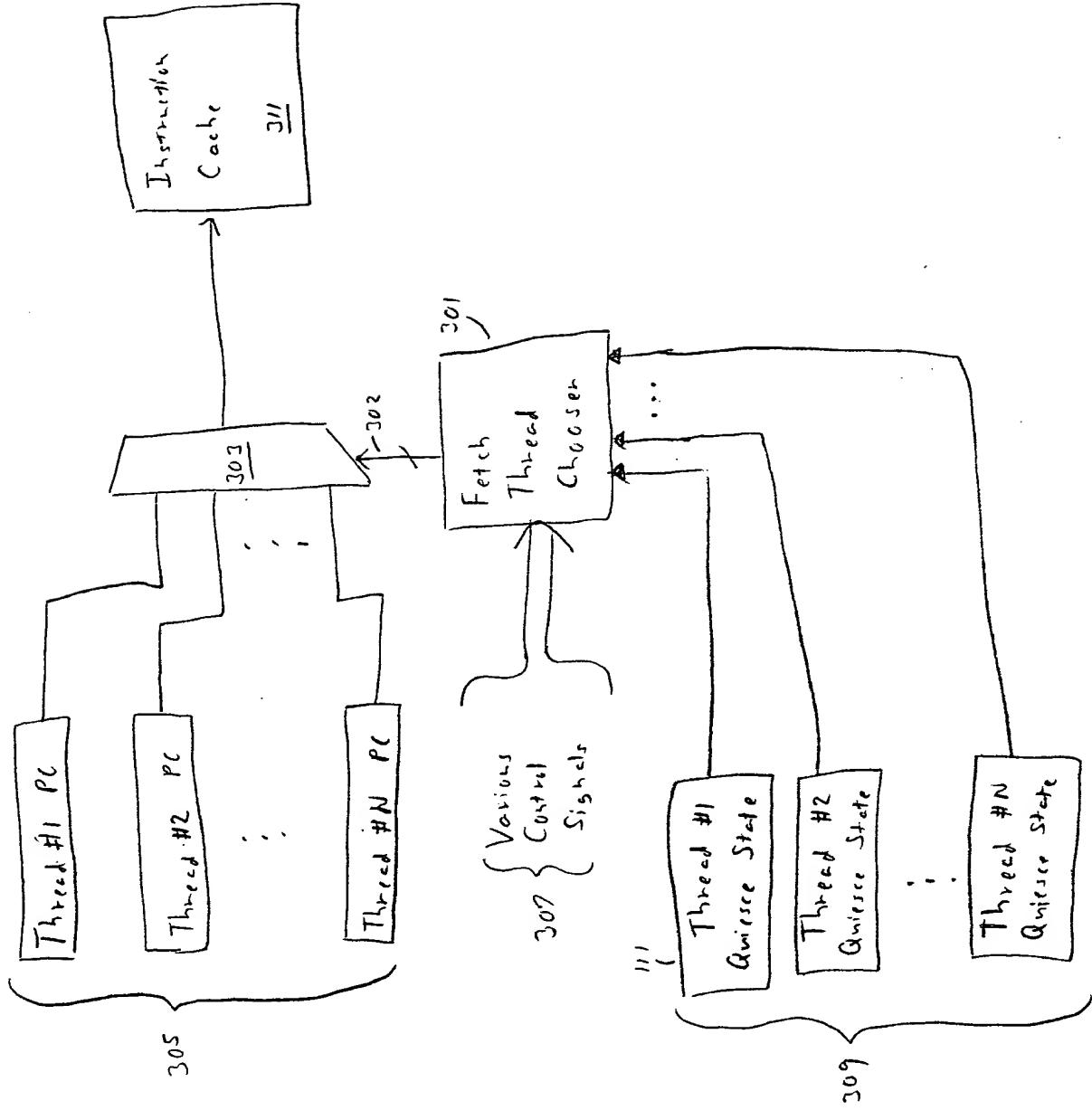
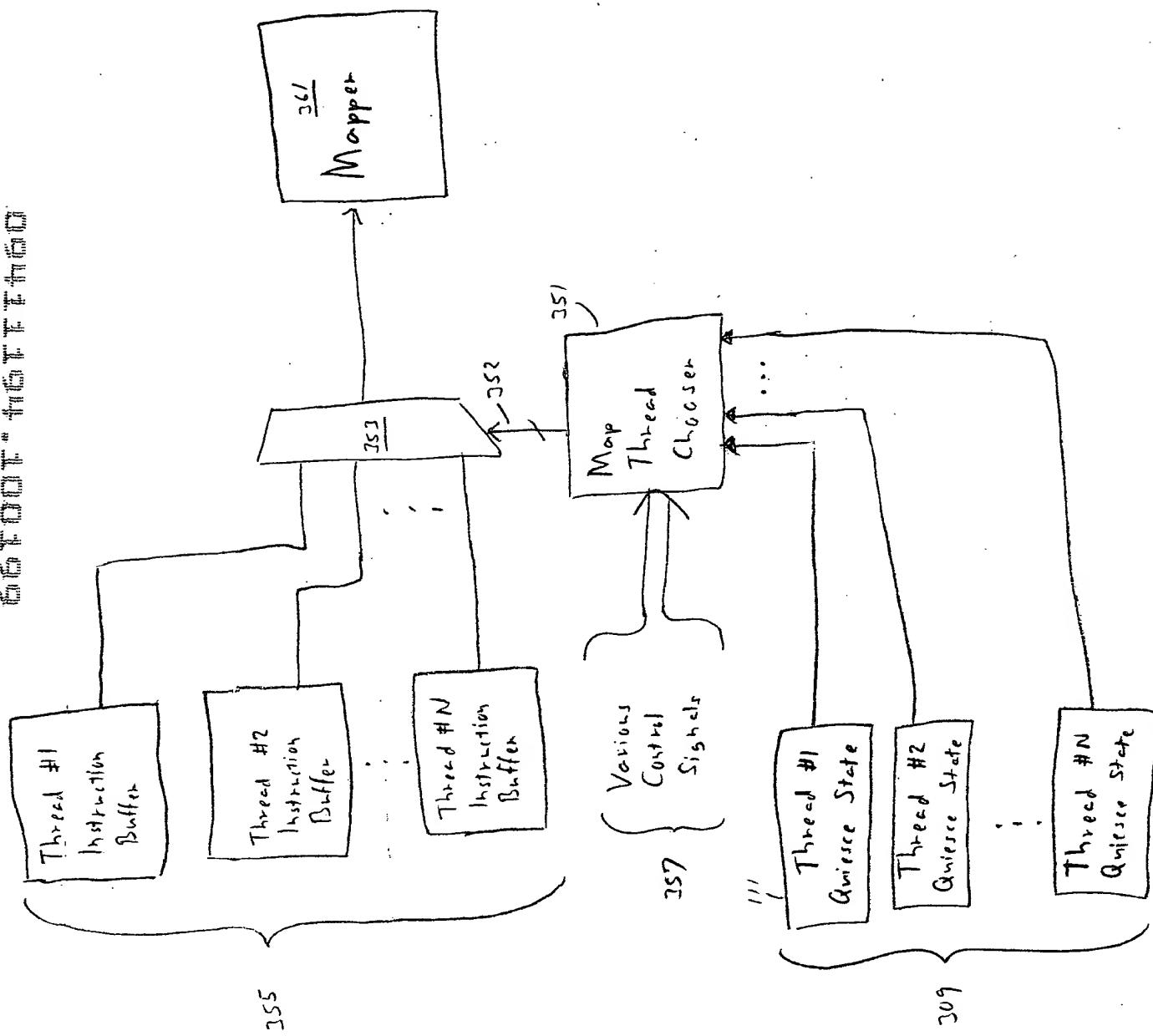


Fig. 3

Fig. 4



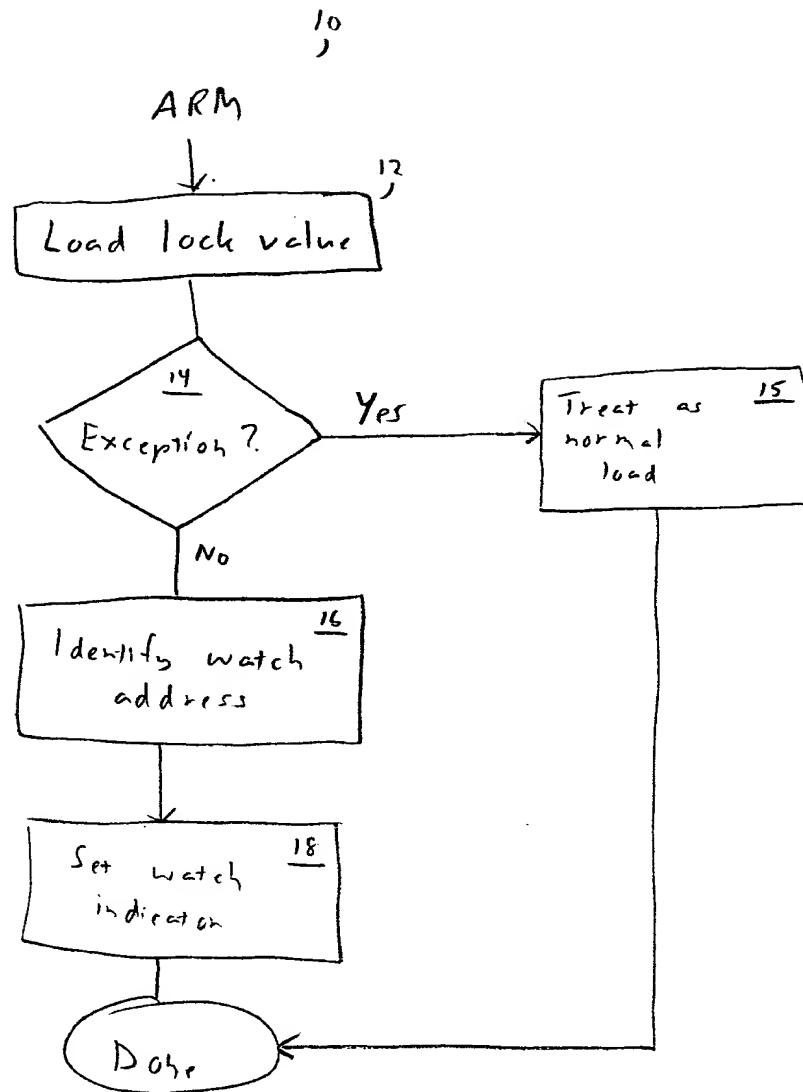


Fig. 5

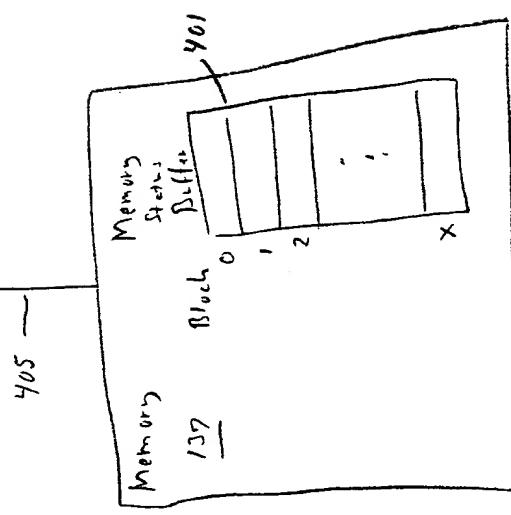
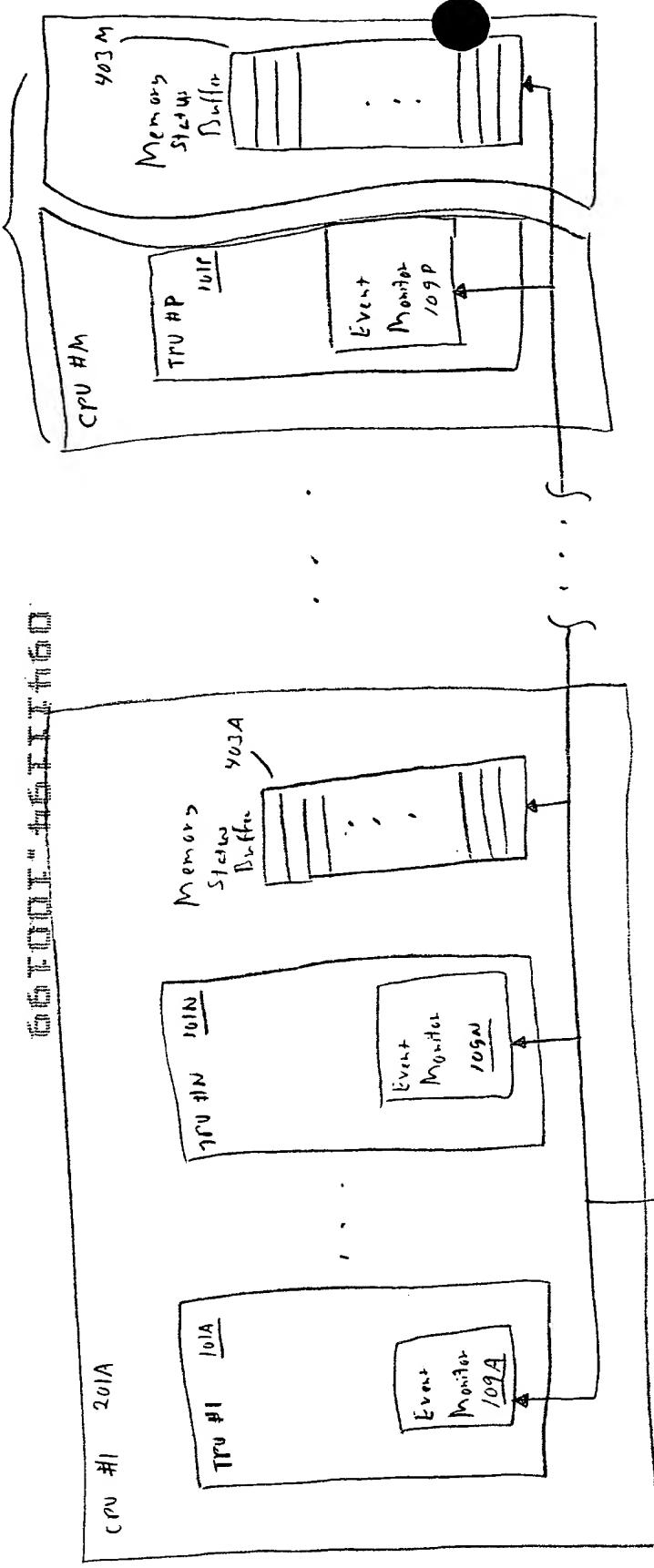
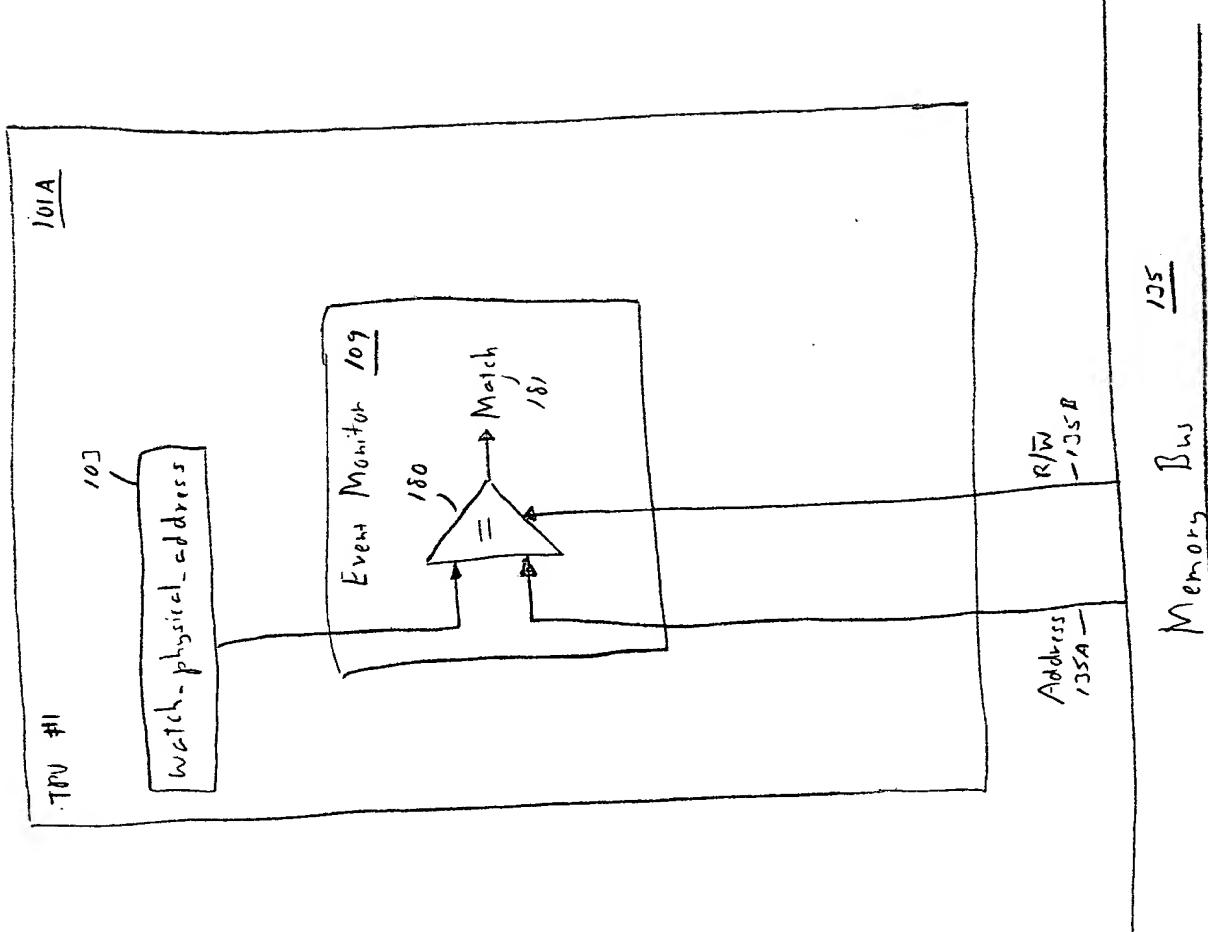


Fig. 6

Event Monitor Architecture



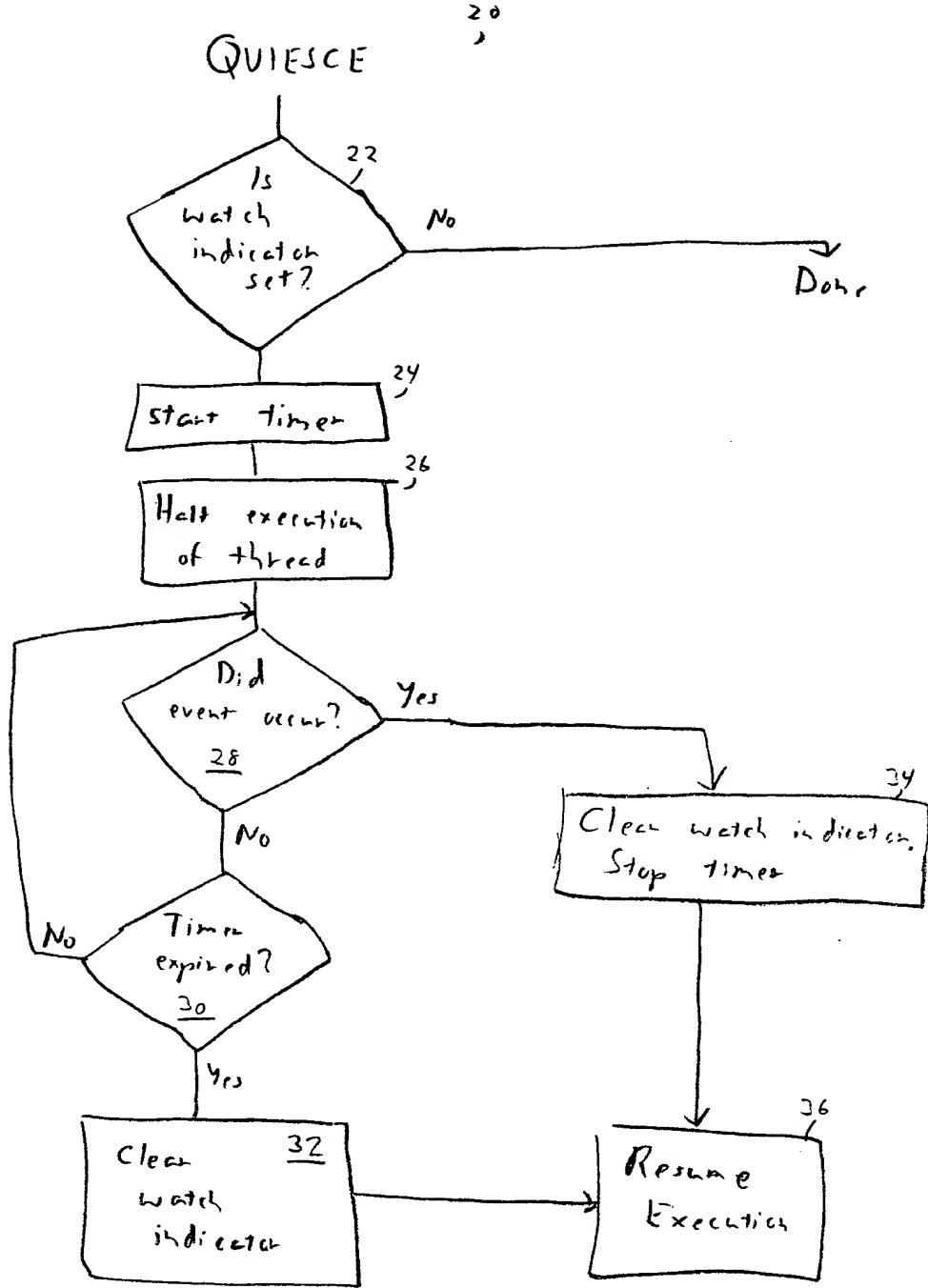


Fig. 8

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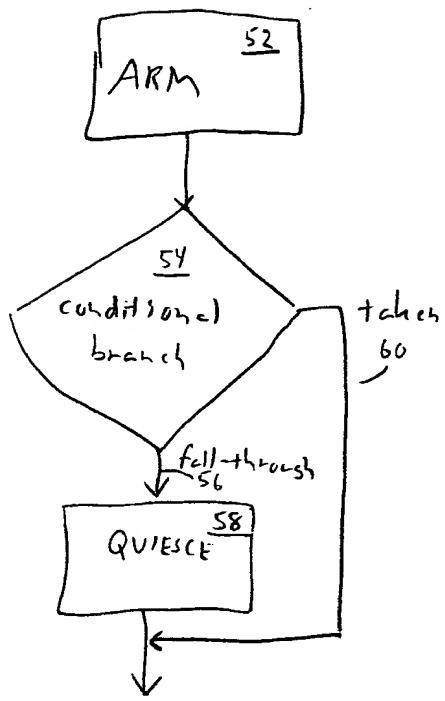


Fig. 9